

Rev	Zone	Revisions Description	Date	Approved

Latch up, cable contacts down, pin one to left, pinouts reversed from MCA, near top of board.

Reference = 2.5V 1% 50ppm
 Bias = control voltage * 1000V
 Range approx 500V-700V, about 18V/turn.

U3 is an inverting stage which regulates VDRIVE to match the HV to the control voltage setting. A higher VFEEDBACK results in a lower VDRIVE and thus brings the output lower again. R6, C33, R9 slow down the response to < 300 Hz.

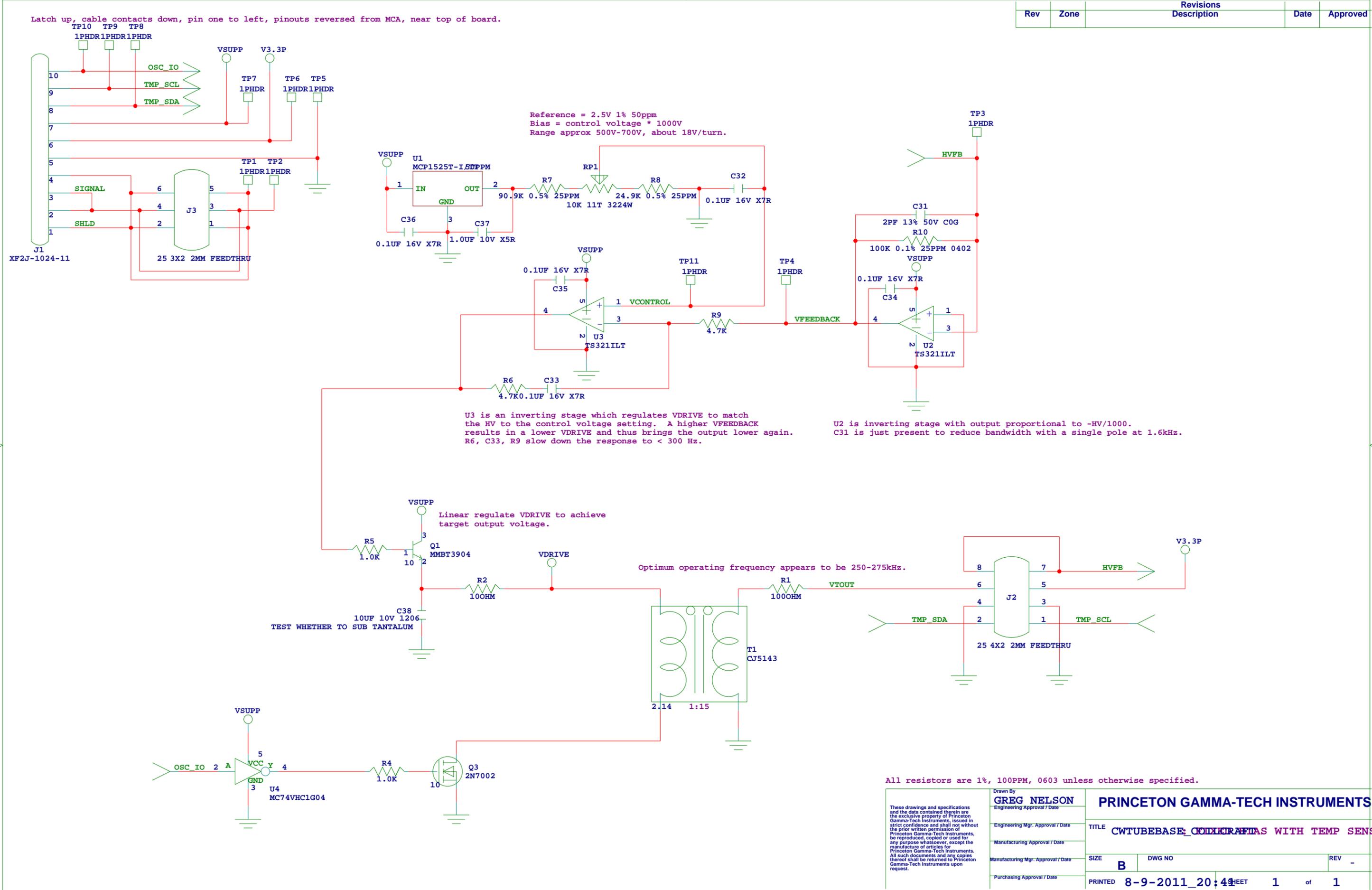
U2 is inverting stage with output proportional to -HV/1000. C31 is just present to reduce bandwidth with a single pole at 1.6kHz.

Linear regulate VDRIVE to achieve target output voltage.

Optimum operating frequency appears to be 250-275kHz.

TEST WHETHER TO SUB TANTALUM

All resistors are 1%, 100PPM, 0603 unless otherwise specified.



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	<p>Engineering Approval / Date</p>	
	<p>Manufacturing Approval / Date</p>	
	<p>Manufacturing Mgr. Approval / Date</p>	
	<p>Purchasing Approval / Date</p>	